

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of	:	EXPEDITED PROCEDURE
	:	Response under 37 CFR 1.116
Pierre Blanchard	:	Confirmation No. 7764
	:	
U.S. Patent Application No. 10/591,178	:	Group Art Unit: 2891
	:	
Filed: August 30, 2006	:	Examiner: Peniel M. GUMEDZOE

For: INTEGRATED CIRCUIT WITH READOUT DIODE OF VERY SMALL DIMENSIONS

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.116

Mail Stop AF
Commissioner for Patents
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Sir:

This paper is submitted in reply to the Office Action mailed August 4, 2008, which was made Final. Applicants respectfully request that the following amendments be entered to place this application in condition for allowance.

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